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**Date:** January 27, 2004

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PATENT  
Attorney Docket No. ASC-058B

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANT(S): Leitz *et al.*  
SERIAL NO.: 10/646,353 GROUP NO.: 2811  
FILED: August 22, 2003 EXAMINER: Not yet assigned  
TITLE: Semiconductor Heterostructures Having Reduced Dislocation  
Pile-Ups and Related Methods

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1. Attached is a copy of the official filing receipt received from the Patent Office for the above-identified application for which issuance of a corrected filing receipt is respectfully requested.
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***Correct Data***

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| 1. <input type="checkbox"/> Applicant's name                    | 1.          |
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| 3. <input type="checkbox"/> Title                               | 3.          |
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| 5. <input type="checkbox"/> Serial Number                       | 5.          |
| 6. <input type="checkbox"/> Foreign/PCT Application Re:         | 6.          |
| 7. <input checked="" type="checkbox"/> Other - Atty. Docket No. | 7. ASC-058B |

Respectfully submitted,



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/646,363	08/22/2003	2811	2238	<del>ASC 58B</del>	6	80	6

A 50-0598

CONFIRMATION NO. 8603

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## UPDATED FILING RECEIPT



\*OC000000011665811\*

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Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

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Vicky Yang, Windham, NH;  
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JAN 13 2004

## Assignment For Published Patent Application

AmberWave Systems Corporation, Salem, NH;

PATENT MARKETING  
TESTA, HURWITZ & THIBEAULT, LLP

## Domestic Priority data as claimed by applicant

This appln claims benefit of 60/405,484 08/23/2002

## Foreign Applications

If Required, Foreign Filing License Granted: 11/13/2003

Projected Publication Date: 04/22/2004

Non-Publication Request: No

Early Publication Request: No

No Docketing Necessary

KFS  
Administrator1-16-04  
Date

Reviewed &amp; Approved

\_\_\_\_\_  
Resp. Atty\_\_\_\_\_  
Date

## Title

Semiconductor heterostructures having reduced dislocation pile-ups and related methods

## Preliminary Class

257

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